

Application No. 10/657,181  
Amendment dated February 28, 2006  
Reply to Office Action of November 30, 2005

Docket No.: 2519-0122PUS1

**AMENDMENTS TO THE SPECIFICATION**

Please add the following paragraphs after the paragraph ending on page 4, line 2:

--Figure 6 illustrates a schematic diagram of using a microchip control unit to implement a frequency-fixed PWM signal generating circuit.--

Please amend paragraph [0016] as follows:

In one embodiment of the present invention, the frequency-fixed PWM signal generating circuit 22 may be implemented by a microchip control unit 30, as shown in Fig. 6, which is set through software programs to perform the desired functions according to the present invention. In another embodiment of the present invention, the frequency-fixed PWM signal generating circuit 22 includes a frequency controller 23 and a PWM signal generator 24, as shown in Fig. 3. More specifically, the frequency controller 23 provides a frequency control signal FC for determining the frequency of the PWM signal S2 generated by the PWM signal generator 24. Based on the duty cycle reference voltage V1 from the duty cycle converting circuit 21 and the frequency control signal FC from the frequency controller 23, the PWM signal generator 24 generates the PWM signal F2 having the duty cycle D2 and the frequency F2.--

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